Lab 1: Ohm’s Law, KVL, and Voltage Divider Rule using Series Circuit

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| Submitted By | Name: Townim Faisal  ID: 1721327042  Section: 08  Group: 07 |

**Experiment 1**



Figure: circuit for experiment 1

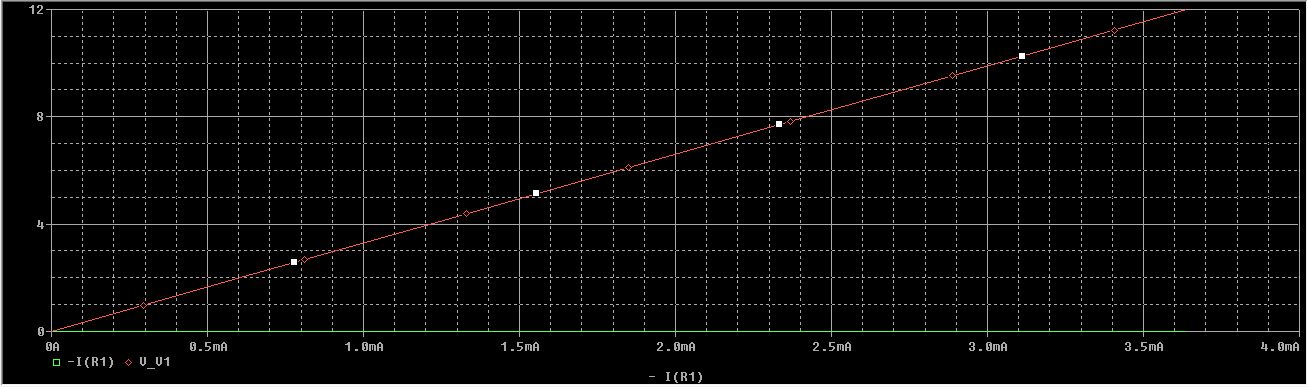


Figure: Plot V(voltage) vs I(mA)

**Experiment 2**

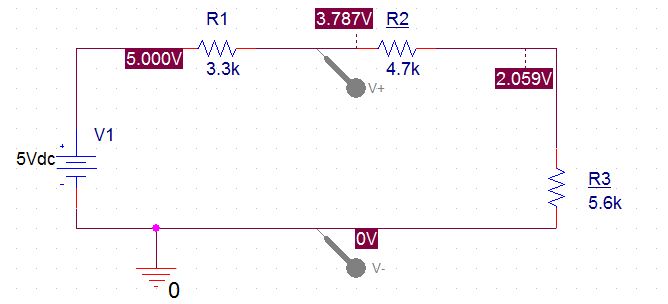


Figure: For 5 volt

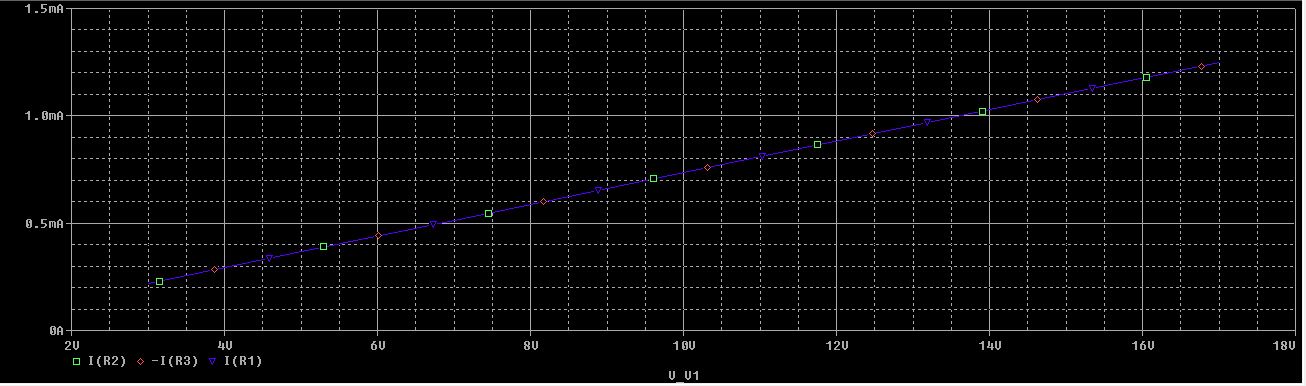


Figure: Voltage source vs I\_R1, I\_R2, I\_R3

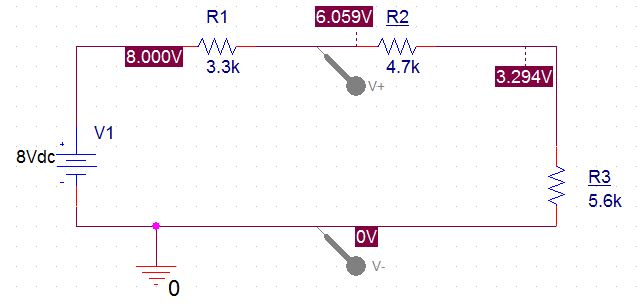


Figure: for 8V

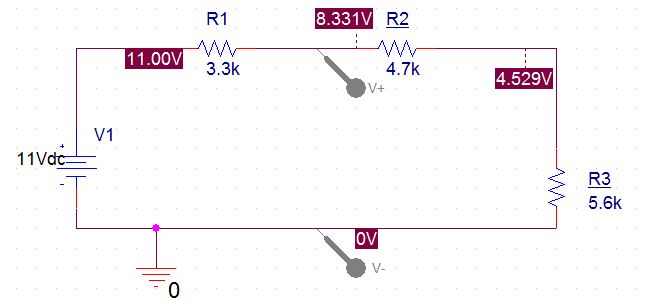


Figure: For 11 V

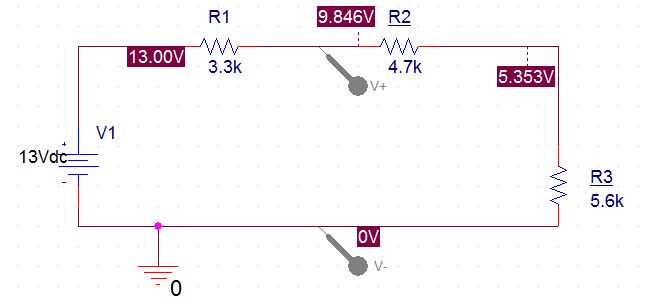


Figure: For 13 v

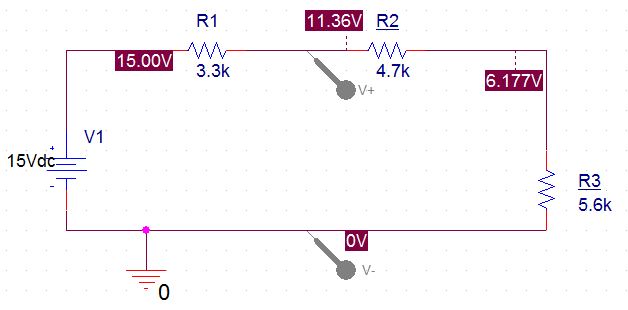


Figure: For 15v